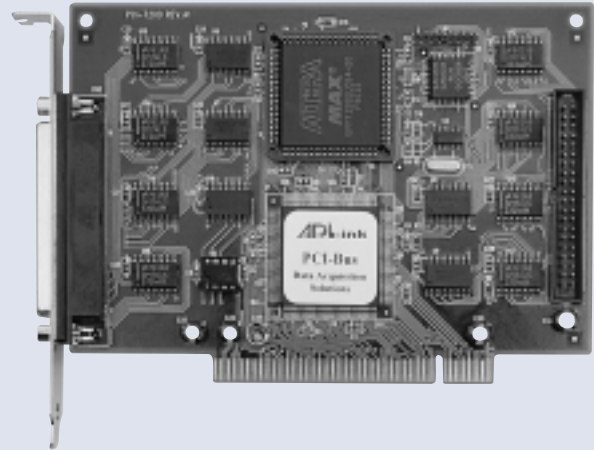


PCI-7200

12MB/s High Speed
32-CH DI, 32-CH DO Card

Features

- 32-bit PCI-Bus with bus mastering DMA
- 32 channels of digital inputs
- 32 channels of digital outputs
- Up to 12M Bytes per sec data transfer rate
- On-board internal timer pacer trigger
- Internal timer pacer trigger is controlled by internal timer
- Input sampling rate and digital output rate are controlled by internal timer
- On-board FIFO for both digital input and output
- ACK and REQ for handshaking data transfer
- Compact, half-size PCB
- Rugged DB-37 connector



Specifications

Digital I/O

- Number of DI channels: 32
- Number of DO channels: 32
- Data Transfer Mode:
 - Program I/O
 - Internal timer pacer transfer
 - External I_REG strobe input
 - Handshake data transfer
- Max transfer speed:
 - 3 MHz (12MB/s) by external clock, handshake or external strobe
 - 2 MHz (8MB/s) by internal timer pacer transfer
- I/O signal characteristic: TTL compatible
- Input voltage:
 - Low: 0.V min., 0.8V max.
 - High: +2.0V min.
- Input load:
 - Low: +0.5V @ -0.6mA max.
 - High: +2.7V @ 20 μ max.
- Output voltage:
 - Low: 0.V min., 0.5V max.
 - High: +2.7V min.
- Output driving capacity:
 - Low: +0.5V @ max. 24mA sink
 - High: +2.4V @ max.-3.0mA source

Programmable Counter

- Device: 82C54, based on 4MHz
- Timer 0: DI clock source
- Timer 1: DO clock source
- Timer 2: Base clock of timer 0/1

General Specifications

- Connector: one DB-37 female connector and one 40-pin header
- Operating temperature: 0°~60°C
- Storage temperature: -20° ~ 80°C
- Humidity: 5~95%, non-condensing
- Power requirement: +5V @ 720mA typical
- Dimensions: 148 mm x 102 mm

Termination Boards

- ACLD-9188 • ACLD-9137
- DIN-37D

Ordering Information

PCI-7200

12 MB/s High Speed 32-Ch DI, 32-CH DO Card (includes one 40 to 37-pin PC back panel, ACL-10437)

Note: The PCI-7200 is shipped with software development kit for DOS/Win-95/98/NT/2000, PCIS-LVIEW and PCIS-VEE.

Pin Assignments of the 40-pin and 37-pin Connector

